

Appl. No. 10/034,462
Amdt. dated May 26, 2005
Reply to Office action of March 8, 2005

REMARKS/ARGUMENTS

Applicants have received the Office Action dated March 8, 2005, in which the Examiner (1) rejected claim 1 under 35 U.S.C. § 102(b) as being anticipated by Micka (U.S. Pat. No. 6,611,901); (2) rejected claim 2 under 35 U.S.C. § 103(a) as being unpatentable over Micka in view of Roy (U.S. Pat. No. 6,321,331); (3) objected to claims 3-15 as being dependent upon a rejected base claim, but otherwise allowable; and 4) allowed claims 16-35. Applicants traverse the rejections of claims 1 and 2 for the following reasons.

Claim 1 requires an "on-chip logic analyzer. Applicants find no teaching whatsoever in Micka of a logic analyzer that is "on-chip." The Examiner identified col. 5, lines 30-35, in Micka for this teaching but that passage, or elsewhere in Micka, says nothing even reminiscent of an on-chip logic analyzer. Claim 1 also requires that data that is stored in the on-chip memory is combined with "a timestamp field representing the number of cycles since the previous stored operation." For this limitation, the Examiner identified col. 5, lines 21-42. Applicants, however, find no teaching or suggestion in this passage or elsewhere in Micka for the claimed "timestamp field." For either or both of these reasons, claim 1 and claim 2 (which depends on claim 1) are patentable over Micka.

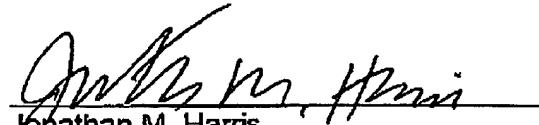
The Examiner used Roy in addition to Micka in rejecting claim 2 as obvious. Roy, however, does not disclose the type of "timestamp field" recited in claims 1 and 2, that is, one that represents "the number of cycles since the previous store operation." Micka does not disclose the "timestamp field" as recited in claim 1 as explained above. For this reason, Applicants respectfully traverse the Examiner's obviousness rejection of claim 2.

Applicants respectfully request reconsideration and that a timely Notice of Allowance be issued in this case. It is believed that no extensions of time or fees are required, beyond those that may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, such extensions are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required (including

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fees for net addition of claims) are hereby authorized to be charged to Hewlett-Packard Development Company's Deposit Account No. 08-2025.

Respectfully submitted,



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